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techniques. When the amount of impairments on the system are $-4 \ -2 \ 0 \ 2 \ 4 \times 10^{-4}$ $-4 \ -2 \ 0 \ 2 \ 4 \times 10^{-4}$ Figure 2. The dashed lines are the

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decision boundaries for symbols, circles are the ideal symbol locations. known, they can be analytically calibrated by modeling quadrature imbalance and non-linear compression behaviors.

Digital Calibration of RF Transceivers for I-Q Imbalances ...

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Analog Circuits and Signal Processing: IQ Calibration ...

The design was fabricated in a 0.35- μm CMOS process and dissipates 160 mW from a 3-V supply during on-line calibration, and 95 mW during

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normal receiving. The die area is 4 mm². View

A Low-Complexity I/Q Imbalance Calibration Method for ...

IQ mixer, an analog component for which calibration is necessary to achieve optimal performance. In this paper, we introduce techniques originally developed for wireless communication applications to calibrate upconversion and downconversion mixers. A Kalman filter together with a controllable carrier frequency offset calibrates both mixers

IQ Mixer Calibration for Superconducting Circuits

The calibration circuit was designed using 65 nm CMOS standard process. Assuming 1% calibration accuracy the required resolution of T-ADC and T-DAC can be estimated as 9 bit. Since T-DAC serves as the reference DC source in the calibration it is the most challenging part of the project.

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